Shift left 2

Memory MemData

Write data

Instruction [15–11]

Instruction [15–0]

Instruction register

Memory register

Instruction [15–0]

Instruction [25–0]

Instruction [25–21]

Instruction [20–16]

Instruction [15–11]

Instruction [15–0]

Instruction [31–26]

Instruction [31–21]

Instruction [20–16]

Instruction [15–11]

Instruction [15–0]

Instruction [31–0]

Jump address [31–0]

Shift left 2

PC [31–28]

Jump

Address EPC